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The test particles method (TPM) as a version of the Monte-Carlo statistic method for the stationary statement is considered. Nonparalleling a sequential algorithm of the TPM as large independent subproblems (LISs) has been performed at previous stages. Testing the LISs algorithm using the problem of an internal flow through Laval nozzle followed by the environmental spraying jet has demonstrated a high degree of the efficiency and the algorithm acceleration. However, testing with a problem of an internal flow past has revealed LISs significant disadvantages. It has been found that the algorithm developed has violated in fact some TPM principles resulting in poorly determined velocity fields in the course of iterations. A demand arose for construction of a parallel algorithm to accelerate the determination of velocity fields. Thus, the algorithm for paralleling on statistically independent tests (PSIT) has been developed to take into account velocity disturbances in simulating trajectories of test particles on each operational cores in a single formable field. PSIT reduces to a series of parallel tests con-

ducted singly on each core. Following computations, the data are synchronized between cores, namely, an exchange of accumulated variations in velocity disturbances of field molecules. Subsequent drawing trajectories on all of the cores is conducted on a single renewed velocity field. After all of the tests the summary characteristics due to computational meshes are transferred to one of cores where moments of the disturbance function are averaged on the summing time of residence of particles into meshes. Using the PSIT algorithm, test computations are made using a multi-core processor. The results obtained are compared with the analogue data of the TPM sequential algorithm, as well as the one-core computational results. It is established that the number of cores used is not affected the results obtained and affected only the computational time when using the PSIT. The time for various versions of the PSIT updated algorithm to minimize the computational time due to reduction of the number of exchanges between cores is fully considered in the paper.

... () [1]
 ... () [2, 3]

... [4, 5].

[6 – 11].
 ... () [8]
 ... [12, 13].
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[12].
 ... ()
 [14].

[15].

() « -4»

$L=23$ $D=3$

$V_\infty \approx 7,8 \text{ /c,}$ $T_w = 300$ (

$t_w \approx 0,01$).

4401-81 [16].

[17].

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[17]

[18].

10^6 [17],

10^6

10^0

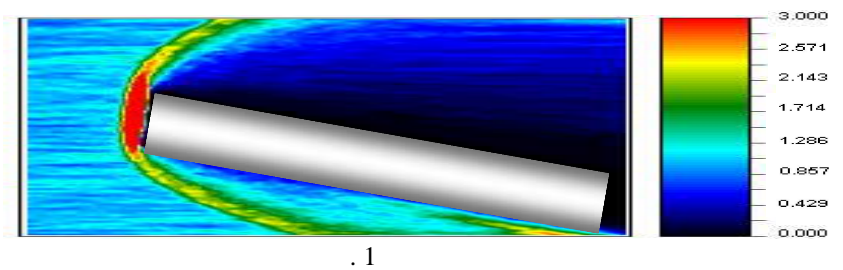
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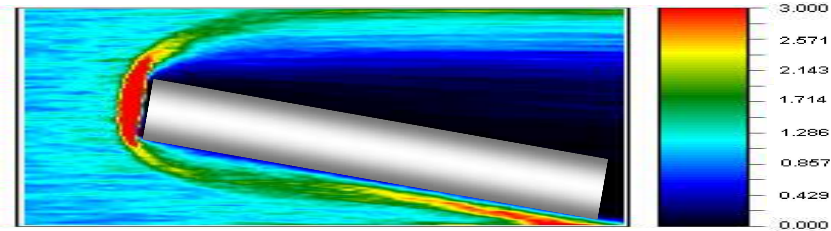
1- 1 2

ρ/ρ_∞ ($\rho_\infty -$

90

1- 4- 1-





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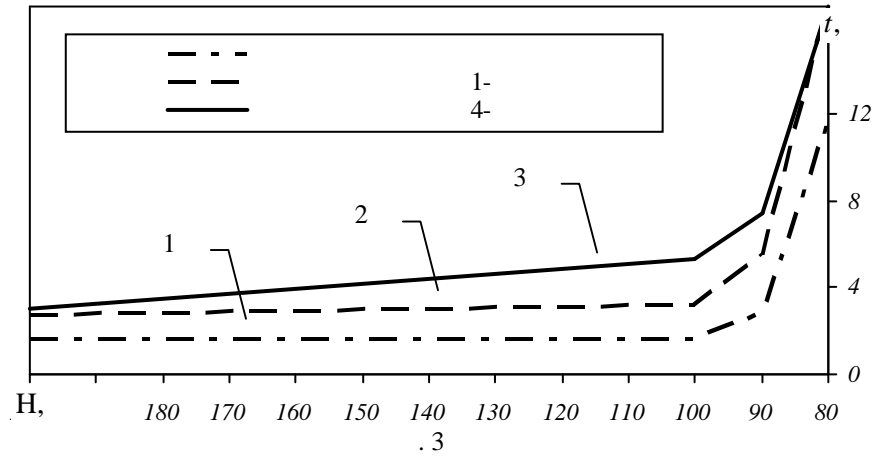
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[17].

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Intel Core 2 Quad Q8400 2,66 GHz
 4 GBytes PC2-6400 (400 MHz) 2x2048 Mbytes ASUS P5KPL-AM
 3. : 200
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(90 - 80).

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80 6 90 80 , 3, 200 (1)

$$Kn_{\infty} = 10^{-3}$$

1 2 1.

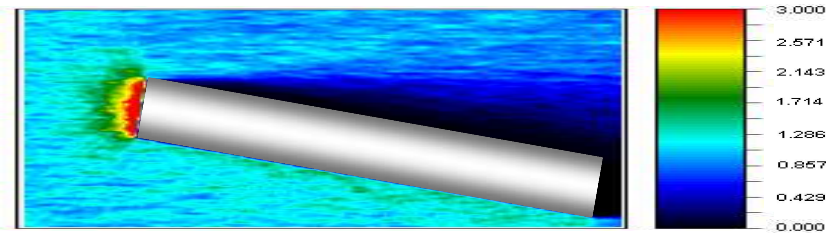
MPI

ρ/ρ_{∞}
200

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[17].

« -4 »



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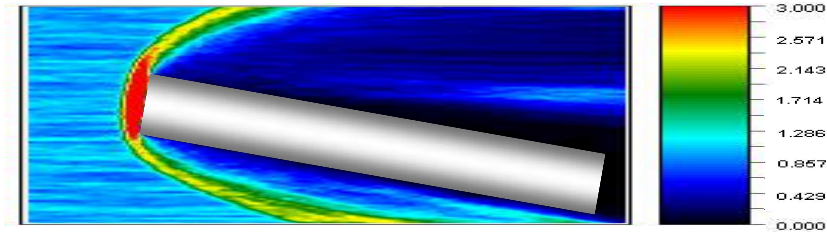
80

4-

3-

ρ/ρ_∞

5.



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10^5 10^6

: $H = 80$
 $1,5 \cdot 10^8$ (10^6).

MPI

N_k

N_k

N
 N

N_k

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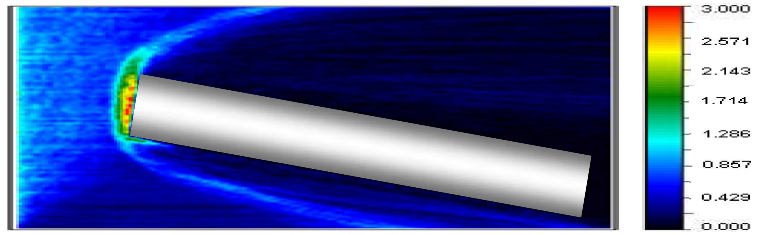
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 1- 4- (2 3 1, -
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 (5, 6). -
). (, 10- , 100- 1000- -
 (, 80 , 1), -
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 1

<i>H</i> ,	,	10-	100- -	1000- -
80	17,03	5,45	4,01	3,77

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 80 2. -
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<i>H</i> ,	,	10-	100- -	1000- -
80	17,03	17,28	18,79	22,2

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 ($H > 80$,
) $5 \cdot 10^6$
 (. .) $1,5 \cdot 10^6$
 (6).
 5 6.



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 10^5 , 10^6 , 80
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1. / . . - . : ,1981. - 319 .

2. (-) -
3. / . . . //-1966.- .167, 5.- .1016-1018. -
- VIII (-)/ . . . // -
- .- .,1986.- .81-85. -
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16.03.15,
20.05.15